

Fig. 1A

	Supervisory	Enable	Disable	Priorities	Interrupts
Stream 1	●		●		
Stream 2	●				●
Stream 3			●		

Control Authorization for Stream 0

Fig. 1B

Integer 1	Integer 2	Integer 3	Integer 4	Float Pt. 1	Float Pt. 2	Float Pt. 3	Float Pt. 4	Branch Unit 1	Branch Unit 3	Branch Unit 2	Branch Unit 4
●	●	●						●			

Hard Resource Assignment for Stream 0

Fig. 1C

Execution
Interrupt
Integer
Floating Point
Branch

●	●	
	●	●
●	●	●
●		
	●	●

Priorities for Stream 0

Fig. 1D

Master	Enabled/Disabled	Sleep Mode
●	●	

Control Indicators for Stream 0

Fig. 1E

Original Stream
Stream 1

Forked Stream
Stream 2

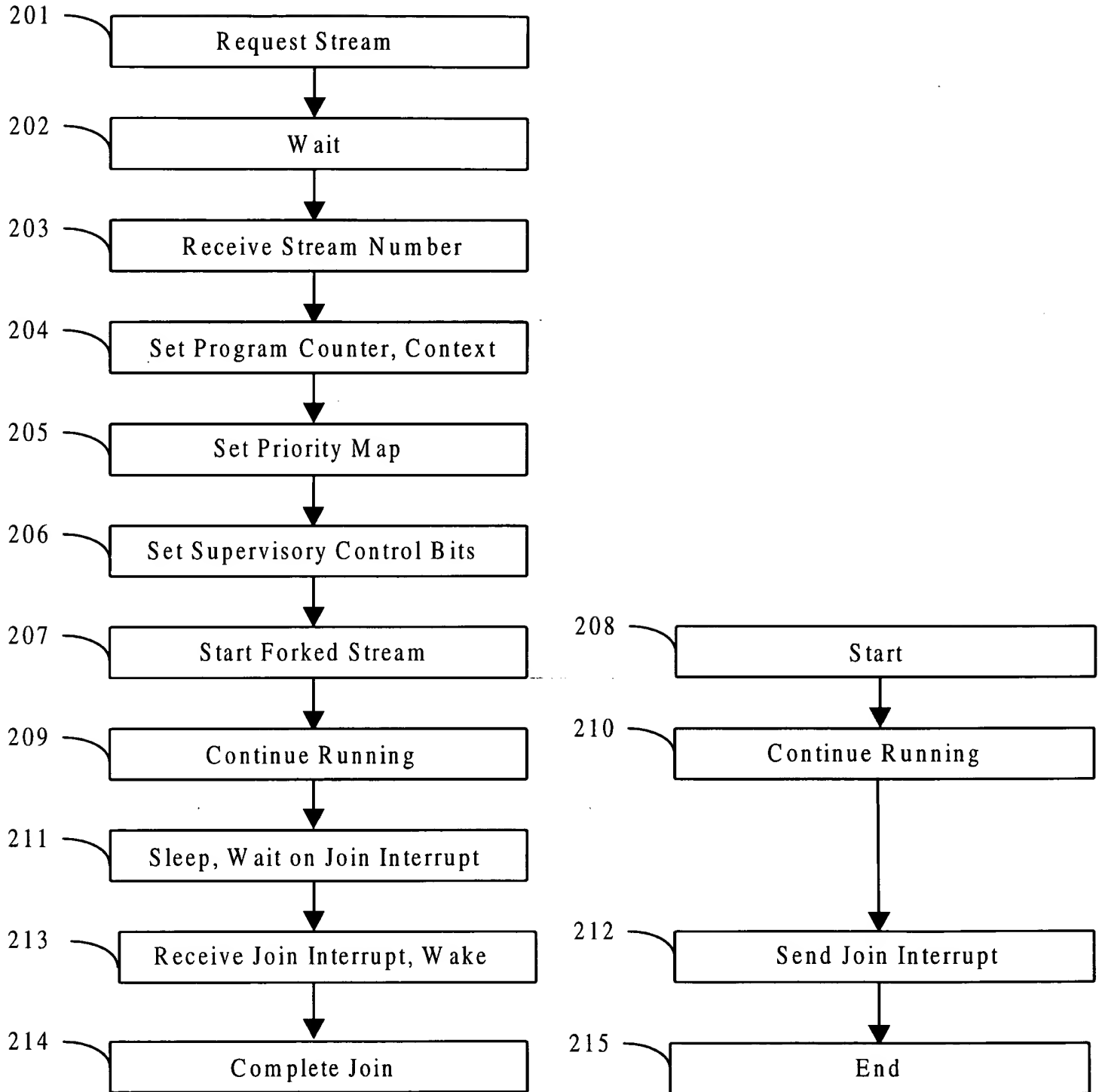


Fig. 2A

Original Stream
Stream 1

Forked Stream
Stream 2

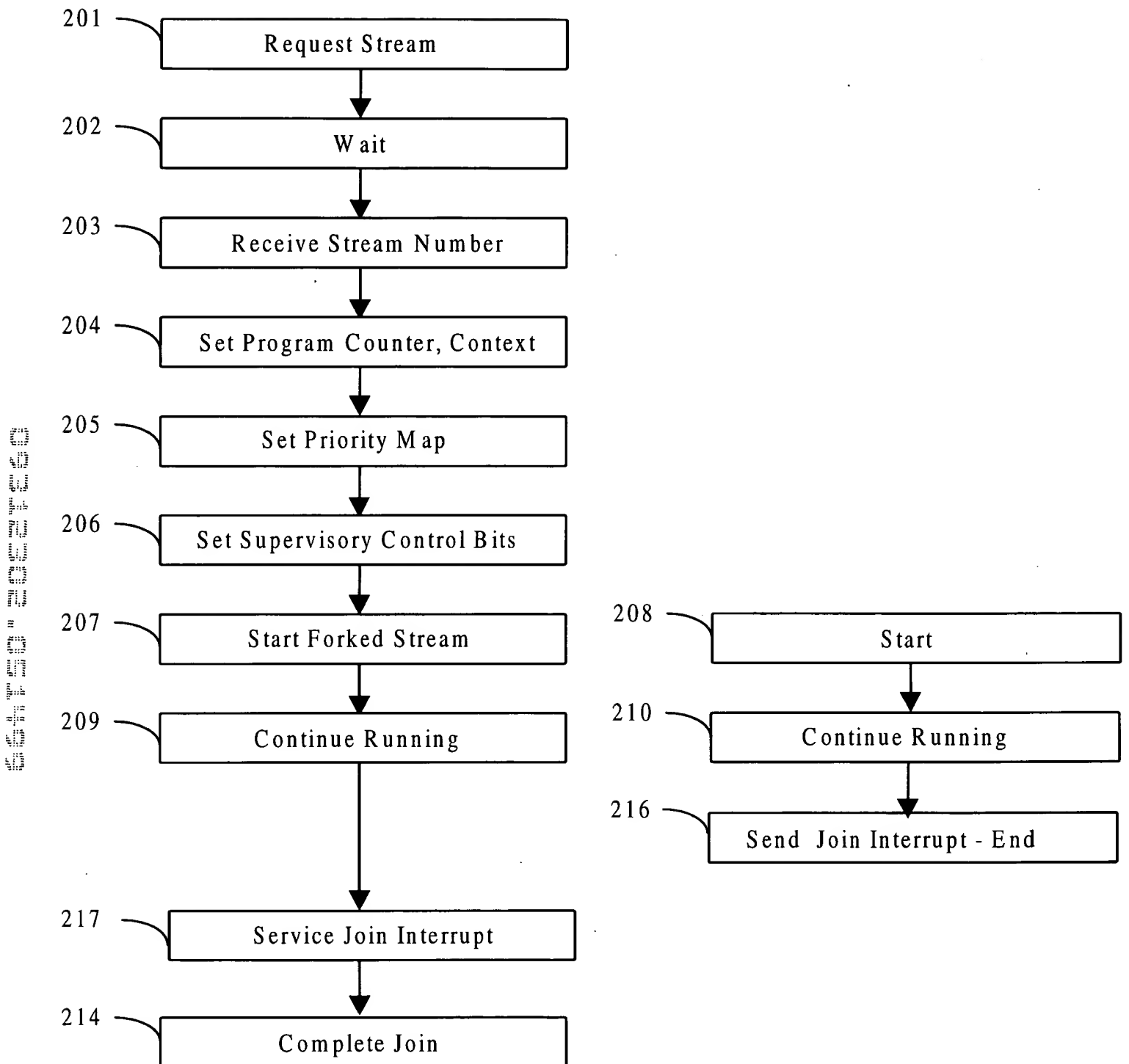


Fig. 2B

Original Stream
Stream 1

Forked Stream
Stream 2

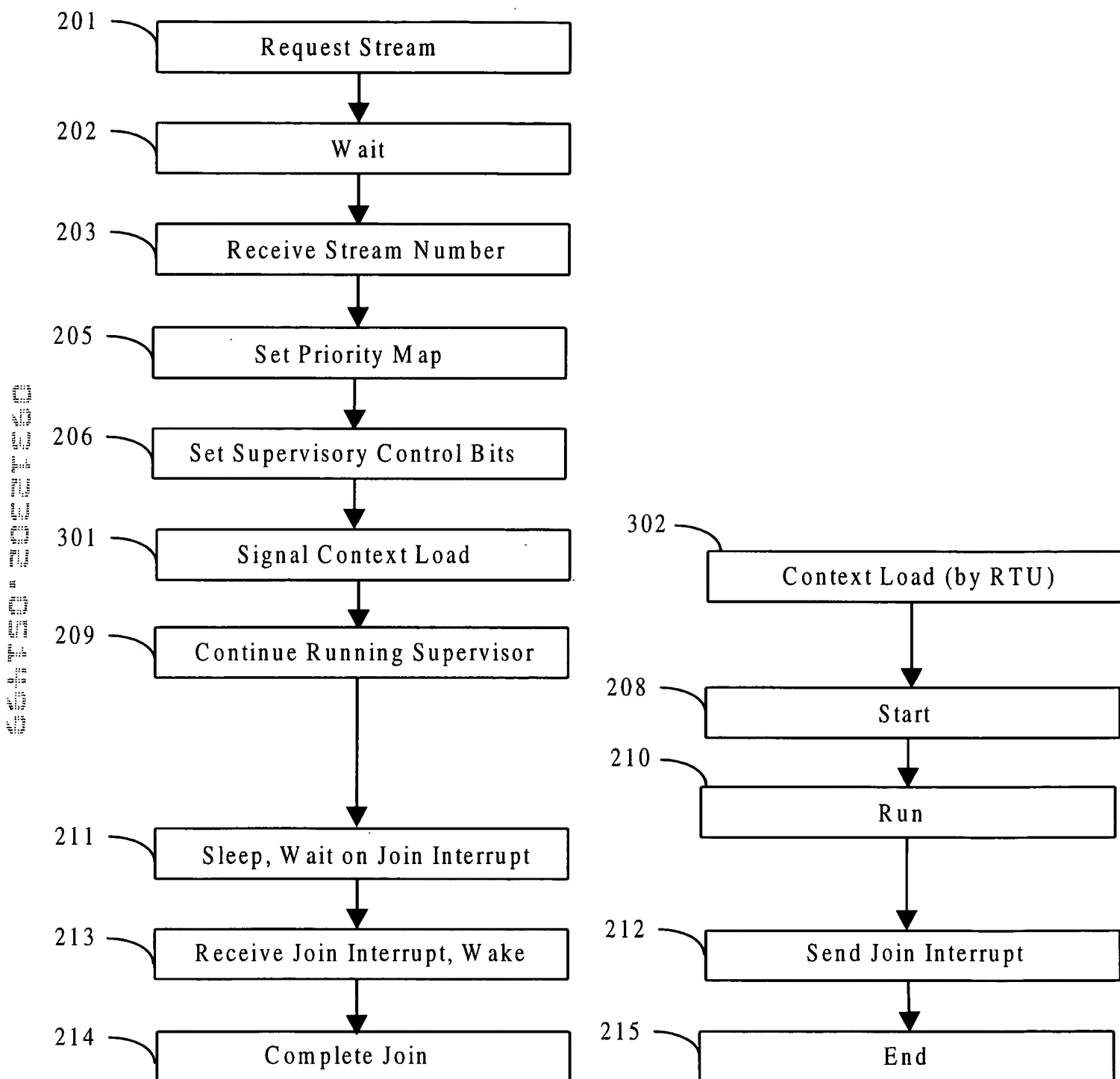


Fig. 3

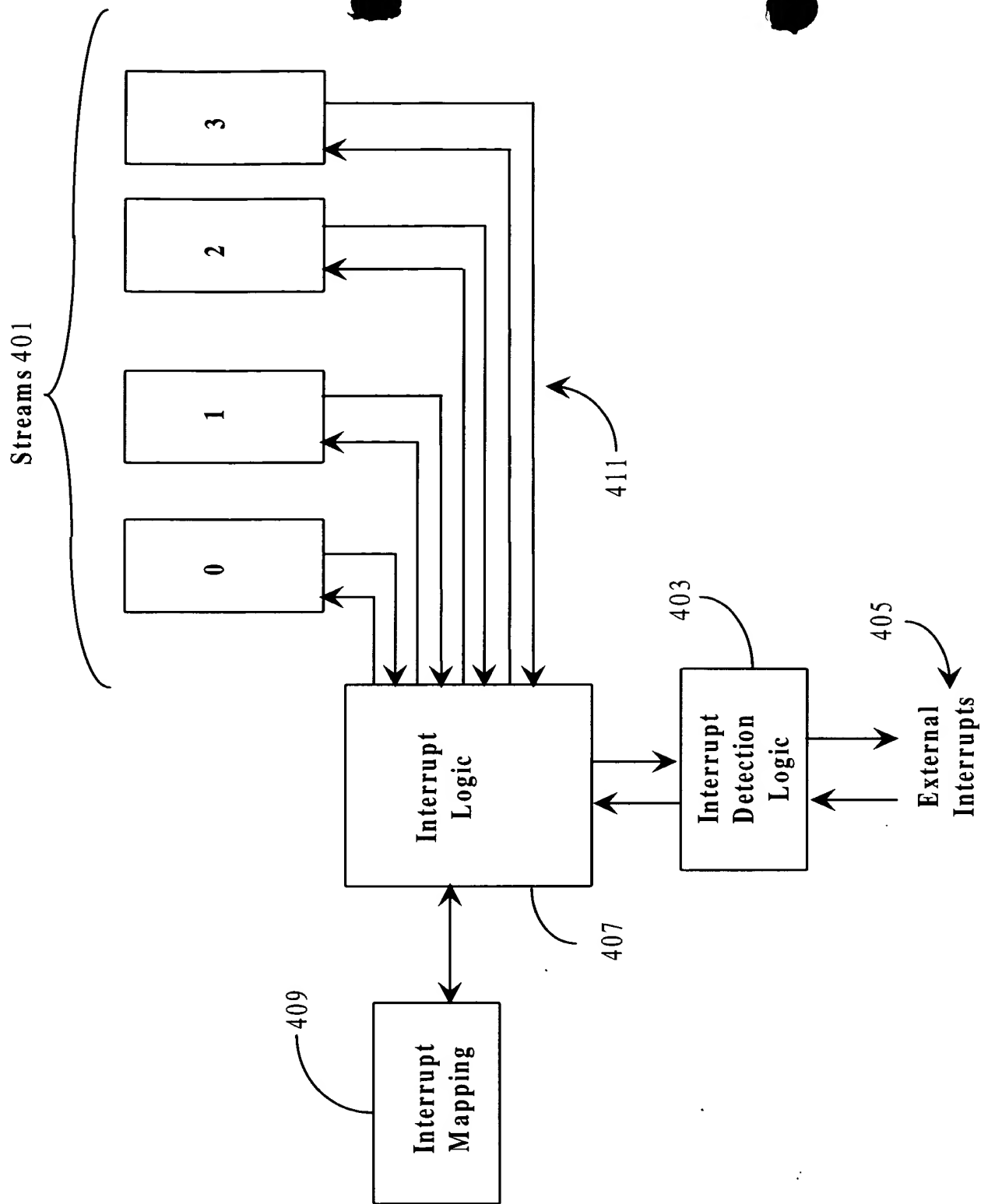


Fig. 4